Jacob Anthony M. Agito

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Education

Mindanao State University - Iligan Institute of Technology   **Iligan City, Philippines**
- Bachelor of Science in Electronics Engineering. GPA: 1.39   June 2025
- Magna Cum Laude
- Gawad Chancellor's Award for Academic Excellence
- DOST-SEI Academic Excellence in Science and Technology Awardee
- DOST-SEI and Analog Devices Inc Scholar
- Thesis: Design of a Low Power Digitally Controlled Oscillator for All-Digital PLL Receiver

Experience

Analog Devices   **General Trias City,**
Design   **Philippines**
Verification Intern   June 2024 - August 2024

- Attended the Digital Design Verification Bootcamp and worked with Digital Design Verification Engineers.
- Simulated working and communicating with the Design Team on troubleshooting, bug fixing and design process.
- Worked with an RTL design called Elevare and created a verification code for the RTL using SystemVerilog.
- Attended the Analog/Mixed Design Verification Seminar and guide.

Leadership & Activities

Junior Institute of Electronics Engineers of the Philippines - Iligan Bay Chapter   **Iligan City, Philippines**
Head of Academic Events Committee   May 2024

- Organized different academic competitions namely Circuit Crunch Challenge: Circuit Analysis, Troubleshooting and Design, Datathon 2024: Programming Competition, Interscholastic Quiz Show.
- Worked with the other committee heads and members from different levels, programs, and schools within JIECEP-IBC.

Voltage/Current Reference IC Design Training   **Iligan City, Philippines**
Member   June 2025 - July 2025

- Collaborative effort with the MSU-IIT Department of Electronics Engineering Faculty and Masters Students inviting students from other universities to undergo training and have them experience using Cadence Software mainly Cadence Virtuoso.
- Participated in making the Lecture Slides and notes for the Faculty to use for the training.

Skills & Interests

- Integrated Circuit Schematic Design and Layout using Cadence Virtuoso.
- Digital Design Verification using Cadence Spectre, SimVision, and SystemVerilog code.
- Used MATLAB, SOLIDWORKS, LaTeX, AutoCAD, HSpice, LTSpice, VSCode, Microsoft Office Suite and Google Suite.
- Assembly and soldering of circuits on breadboard and circuit boards.
- Built 5 personal computers.
- Studying C++ and Python.